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## **Prof. Yukiharu Uraoka**

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Yukiharu Uraoka was born in Tokushima, Japan, in 1961. He received the B.S., M.S., and Ph.D. degrees in electrical and electronics engineering from Toyohashi University of Technology, Aichi, Japan, in 1983, 1985, and 1994, respectively.

In 1985, he joined Panasonic Company, Ltd., Osaka, Japan, where he has been engaged in the research of the reliability of CMOS devices and low temperature poly-Si display devices. Since 2009, he has been a Professor of the Graduate School of Material Science, Nara Institute of Science and Technology, Nara, Japan.

His current research interest involves the fabrication of thin film devices using Si film, metal oxide film for flexible device. He is also interested in memory or LSI for AI (Artificial Intelligence), with a particular focus on advanced LSI process technologies to support high-performance computing and energy-efficient system architectures. He is a fellow of IEEE.