



Dr. Sang Ho Jeon

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Dr. Sang Ho Jeon earned his Ph.D. in Materials Engineering from Seoul National University, where his research focused on oxide TFT devices and advanced simulation techniques for material optimization. After completing his doctorate, he joined Samsung Display and broadened his research scope to include various light-emitting devices such as OLEDs and LEDs. Over the course of 11 years in the industry, he has developed extensive expertise in multiscale simulation, conducting atomic-scale studies using density functional theory (DFT), nano-scale investigations through molecular dynamics, and micro-scale device simulations with TCAD and kinetic Monte-Carlo (kMC) methods. Currently serving as a principal engineer, he continues to drive innovation in display technology by integrating theoretical research with practical applications.