



Prof. Jaemyung Lim

Hanyang University, South Korea

Jaemyung Lim received the bachelor's degree in electrical and computer engineering from Hanyang University, Seoul, South Korea in 2011, and the M.S. and Ph.D. degrees in electrical and computer engineering from Georgia Tech, Atlanta, GA, USA, in 2013 and 2017, respectively. From 2004 to 2007, he was a Senior circuit designer in Apple Inc., Cupertino, CA, USA. He currently is a assistant professor at Electronic engineering in Hanyang University, Seoul, South Korea.

His scholarly pursuits revolve around various areas including display driver ICs for OLEDoS and LEDoS, ultrasound imaging systems, and power management IC design. Furthermore, he harbors a keen interest in leveraging deep learning and generative AI for design methodology and automation.