

<b>Session Title:</b>	<b>14. Advanced Driving Circuits</b>
<b>Session Date:</b>	<b>August 24 (Wed.), 2022</b>
<b>Session Time:</b>	<b>10:50-12:25</b>
<b>Session Room:</b>	<b>Room F (313)</b>
<b>Session Chair(s):</b>	<b>Prof. Hojin Lee (Soongsil Univ., Korea) Prof. Byong-Deok Choi (Hanyang Univ., Korea)</b>

**[F14-1] [Invited]** 10:50-11:15

**LTPO GIP Driving Technology for Low Power Consumption and VRR**

Jung Chul Kim, Juhn Suk Yoo, Chung Wan Oh, Chung Sik Gong, Han Wook Hwang, and Hyun Jae Kim (LG Display Co., Ltd., Korea)

**[F14-2] [Invited]** 11:15-11:40

**LTPO-Based CMOS TFT Circuit**

Yi Kyoung You (Konkuk Univ., Korea), Sang Yong No, Kyung Ho Kim, Gi Chang Lee (Samsung Display Co., Ltd., Korea), and Kee Chan Park (Konkuk Univ., Korea)

**[F14-3]** 11:40-11:55

**A Highly Stable Scan Driver Circuit based on LTPO TFTs for Mobile Displays**

Ye-Rim Jeong, Eun Kyo Jung, Sara Hong, Hwarim Im, and Yong-Sang Kim (Sungkyunkwan Univ., Korea)

**[F14-4]** 11:55-12:10

**Adaptive Frequency Driving Scan Driver Combined with Logic Circuit based on  $\alpha$ -InGaZnO TFTs**

Jinho Moon, Eseudeo Yun, Yongchan Kim, and Hojin Lee (Soongsil Univ., Korea)

**[F14-5]** 12:10-12:25

**Digital Video's Limitations Are Impeding the Market Transition from 4K to 8K Displays. Modulated Analog Video Is the Way Forward!**

Alex Henzen and Simon Molloy (HYPHY USA Inc., USA)