


# iMiD 2022

The 22nd International Meeting on Information Display  
August 23-26, 2022 / BEXCO, BUSAN, KOREA



Company Name	SILVACO	Company Logo
Address	5F, Star-City Bldg., 140, Gucheonmyeon-ro, Gangdong-gu, Seoul, Korea	
President	ManGyu Hwang	
Website	<a href="https://silvaco.com/ko/">https://silvaco.com/ko/</a>	
E-mail	krsales@silvaco.com	
Telephone	82-2-447-5421	
Fax	82-2-447-5420	
Exhibitor Introduction	Silvaco is a leading provider TCAD, EDA software, and semiconductor design IP, used for process and device development for advanced semiconductors, power IC, display, memory, and SoC design. For over 35 years, Silvaco has enabled its customers to develop next generation semiconductor products in the shortest time with reduced cost. The company is headquartered in Santa Clara, California and has a global presence with offices located in North America, Europe, Japan, China, Taiwan, Korea, and Singapore.	
Exhibit Description	Silvaco has a complete flow from TCAD-to-Signoff for display design. The device characteristics that TCAD produces can be used for a first level of analysis and then special SPICE compact models such as RPI for a-Si TFT or UOTFT for organic and oxide TFTs can be created for SPICE simulation.	
Exhibit Product	<ul style="list-style-type: none"><li>• Victory – 2D/3D TCAD Process &amp; Device Simulation</li><li>• Clever – RC Extractor for Realistic 3D Structures</li><li>• Hipex – Full-chip Rule-based RC Parasitic Extraction</li></ul>	

# iMiD 2022

The 22nd International Meeting on Information Display  
August 23-26, 2022 / BEXCO, BUSAN, KOREA

- Gateway – Schematic Capture
- Expert – Layout Editor
- SmartDRC/LVS – DRC/LVS/Net Physical Verification
- SmartSpice – Circuit Simulator
- Utmost IV– Device Characterization and SPICE Modeling
- SiCure – IR, EM and Thermal Analysis
- Jivaro– Parasitic Reduction for Fast, Accurate Simulation
- Viso – Parasitic Analyzer and Debugger
- Belledonne – Layout Parastic Extraction Comparison
- VarMan – Statistical Variation and Yield Analyzer
- Cello – Cell Library Creation, Migration and Optimization
- Viola – Cell Library Characterization, Validation & Variation Modeling