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Dale Stolzka is the Principal Engineer at the Samsung Display America Lab in San Jose, CA, who leads display standardization and the location's Vision Lab. He leads or contributes substantially to industry standards in HDR, visual and motion artifact characterization, display compression and transport protocols at VESA, MIPI Alliance, ISO/IEC JPEG, IEC TC 100 and TC110 and the SID ICDM. He chairs the MIPI Alliance Display Working Group and the VESA Display Steam Compression Task Group and was a past co-chair of the JPEG Coding, Test and Quality subgroup.. Dale is an active champion of HDR advancement and OLED metrology across platforms.

Mr. Stolzka's experience spans consumer electronics displays, coding systems and advanced transport mechanisms. Before joining Samsung, he has developed computer and consumer electronics systems and mixed-signal system designs at Analog Devices, National Semiconductor, Maxtor and Raytheon. He holds a B. S. degree in Applied Physics and an M. Eng. degree in Materials Science and Engineering, both from Cornell University.