

Effect of Ambient Annealing on Electrical Characteristics of ZnO TFTs with AZO Interlayer

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Zinc oxide (ZnO) thin film transistors (TFTs) have attracted much attention as display drive device due to their high field-effect mobility, low fabrication temperature, transparent and large on/off current ration. But ZnO TFTs have some chronic problem, such as low on current and instability [1]. In order to solve these problems, we applied Al doped ZnO (AZO) interlayer, and confirm the improvement of electrical stability under the positive gate bias stress condition. But the field-effect mobility is observed to decrease slightly, which indicates that the Al atoms in AZO layer can decrease oxygen vacancies but form Al clusters like Al-Al and Al-O affecting the mobility scattering mechanism [2].

In this study, we investigate the ambient annealing effect of ZnO TFTs with AZO interlayer comparing with the conventional ZnO TFTs. All TFTs were annealed in a furnace at 250°C for 1hour in the different ambient of N₂, air and O₂. Figure 1 shows the transfer curve of the ZnO and AZO TFTs according to the ambient. Here, the gate bias is shifted by the difference in threshold voltage between annealing ambient to emphasize the subthreshold properties. The electrical parameters are summarized in Table I. It is known that the native point defects in ZnO like oxygen vacancies, Zn interstitials contribute to the carrier concentration, but can act deep trap sites [3]. When the devices are annealed in the oxygen ambient, the oxygen vacancy can be cured lowering the deep trap density with the carrier concentration [4]. This theory explains well our experimental results like as the sub-threshold slope improvement and the positive threshold voltage shift in the case of Air and O₂ ambient. However, the amount of annealing effect on AZO interlayer TFTs is not the same with the conventional ZnO TFTs, which will be investigated focusing the mobility scattering mechanism.

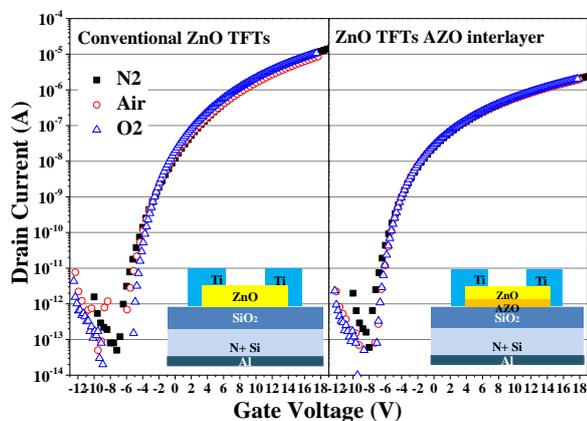


Fig. 1. Electrical transfer characteristics of TFTs with different annealing conditions normalized by threshold voltage.

	Annealing Gas	V _{th} (V)	SS (V/decade)	On current(A) (V _G -V _{th} =20V)
ZnO TFTs	N ₂	-2.54	1.23	1.03x10 ⁻⁵
	Air	-0.22	0.99	8.55x10 ⁻⁶
	O ₂	-0.01	0.74	1.06x10 ⁻⁵
AZO/ZnO TFTs	N ₂	-4.02	1.10	1.59x10 ⁻⁶
	Air	-1.95	0.84	1.49x10 ⁻⁶
	O ₂	-1.76	0.84	1.60x10 ⁻⁶

Table I. Extraction results of electrical parameters in TFTs according to the annealing conditions.

Acknowledgment

References

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