Flexible Metal-Oxide Thin-Film Transistors with Organic Gate Insulators

Hsing-Hung Hsieh¹, Shiuan-Iou Lin¹, William C. Sheets², Su Jin Kang², Wan-Yu Hung¹, Scott Bull², Antonio Facchetti² and Chung-Chin Hsiao¹ ¹Polyera Taiwan Corporation, 4F-11, No. 38, Taiyuan St., Zhubei, Hsinchu 30265, Taiwan Tel.:886-3-560-0690, E-mail: <u>hh.hsieh@polyera.com</u> ²Polyera Corporation, Suite 140, 8045 Lamon Avenue, Skokie, Illinois 60077, USA

Flexible and wearable displays are very attracting opto-electronic devices, which might affect our daily life considerably [1-3]. Yet thin-film transistors (TFTs) with high good electrical characteristics and mechanical flexibility are essential to develop this new technology. Traditional silicon-based TFTs are mature backplane technologies, but the mechanical flexibility is questionable. Organic semiconductor-based TFTs are known to exhibit good flexible properties [4-5], but their electrical performance such as field-effect mobility is somehow limited. An alternative path to achieve high-performance optoelectronics is to use TFTs based on amorphous oxide semiconductors coupled to organic gate insulators. This combination should ensure both high TFT performance and flexibility because of the amorphous nature of the key TFT materials components. However, existing oxide TFTs with organic gate insulators suffer from several drawbacks such as low mobility, large subthreshold swing, large I-V hysteresis, high off-current, low on/off current ratio, and poor bias stress stability [6].

In this work, we demonstrate a new photo-patternable and thermally stable organic gate insulator platform for high performance IGZO TFTs. The new organic gate insulator exhibits several key technological advantages such as low leakage current (~10 nA/cm² at 2 MV/cm), high breakdown voltages, good mechanical flexibility (functions at radius less than 1 cm), photo-patternability (5~10 μ m), and substantial temperature stability (up to temperature of 300 °C), considerable adhesion, and high transparency (>97% in visible). In addition, we have also explored various TFT device architectures to optimize the device architecture specifically for IGZO transistors with an organic gate insulator. Our devices were first optimized on glass substrates, then the process flow was transferred to plastic substrates, with both platforms exhibiting good and similar performance (e.g., high mobility of ~15 cm²/Vs). Finally, we compared the performance of the Polyera OGI-based IGZO TFTs to those TFTs based on conventional organic dielectrics, demonstrating the great potential of our materials both in term of device performance and manufacturability.

Acknowledgment

The authors would like to express their appreciation to all the colleagues at Polyera Corporation and Polyera Taiwan Corporation who are involved in the metal oxide project.

References

- 1. H.-H. Hsieh, C.-H. Tsai, C.-S. Yang, C.-J. Liu, J.-H. Lin, Y.-Y. Wu, C.-H. Fang, C.-S. Chuang, *Journal of the Society for Information Display*, 21(8), 326 (2013).
- 2. M.-G. Kim, M. G. Kanatzidis, A. Facchetti, and T. J. Marks, Nature Materials, 10, 382 (2011).
- Y. Jimbo, T. Aoyama, N. Ohno, S. Eguchi, S. Kawashima, H. Ikeda, Y. Hirakata, S. Yamazaki, M. Nakada, M. Sato, S. Yasumoto, C. Bower, D. Cotton, A. Matthews, P. Andrew, C. Gheorghiu, J. Bergquist, *SID Symposium Digest of Technical Papers*, 45, 322 (2014).
- 4. M. Noda, K. Teramoto, E. Fukumoto, T. Fukuda, K. Shimokawa, T. Saito, T. Tanikawa, M. Suzuki, G. Izumi, S. Kumon, T. Arai, T. Kamei, M. Kodate, S. No, T. Sasaoka, K. Nomoto, *SID Symposium Digest of Technical Papers*, 43, 998 (2012).
- 5. E. Huitema, F. Touwslager, E. V. Veenendaal, N. V. Aerle, P. V. Lieshout, SID Symposium Digest of Technical Papers, 40, 104 (2009).
- 6. C.J. Chiu, S.P. Chang, S.J. Chang, Thin Solid Films, 520, 5455 (2012).